

**AMENDMENTS TO THE ABSTRACT:**

Please amend the Abstract as follows:

An integrated circuit (14) is provided with a tracing mechanism (10, 12) that is responsive to data access misses to insert a data place holder (32) within a stream of trace data. When the missed date is later returned, this is inserted into the stream of traced data as a later data value (44). Analysis of the stream trace data may subsequently correlate between instructions that gave rise to data misses and the late data that was subsequently returned.

[Figure 3]

**AMENDMENTS TO THE SPECIFICATION:**

A substitute specification is attached.